

FORM PTO-1449				Atty. Docket No. XA-9689A		Appln. No. 10/616355	
<b>LIST OF DOCUMENTS CITED BY APPLICANT</b>							
				Applicant Norikatsu TAKAURA et al.			
				Filing Date Herewith 7/10/03		Group 2813	
<b>U.S. PATENT DOCUMENTS</b>							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
<i>JMS</i>	AA	6,153,490	11-2000	Xing et al.	438	396	—
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
<b>FOREIGN PATENT DOCUMENTS</b>							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
<i>JMS</i>	AL	2000-174225	6/00	Japan			Abstract
<i>JMS</i>	AM	09-036318A	02/97	Japan			
<i>JMS</i>	AN	04-058556A	02/92	Japan			
<i>JMS</i>	AO	02-214155A	09/90	Japan			
	AP						
	AQ						
	AR						
	AS						
<b>OTHER (including author, title, date, pertinent pages, etc.)</b>							
<i>JMS</i>	AT	Kistler, N. et al., "Symmetric CMOS in Fully-Depleted Silicon-On-Insulator Using P+-Polycrystalline Si-Ge Gate Electrodes," Dec. 1993, IEDM 93, pp. 727-730.					
Examiner <i>[Signature]</i>				Date Considered 11/12/04			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							